

# Notice of Allowability

Application No.

10/603,661

Examiner

VIJAY SHANKAR

Applicant(s)

KIM ET AL.

Art Unit

2673

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 06-26-2003.
2. ☒ The allowed claim(s) is/are 1-14, 16-20, Renumber as 1-19.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All b) ☐ Some\* c) ☐ None of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

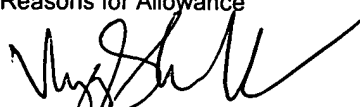
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
VIJAY SHANKAR  
Primary Examiner  
Art Unit: 2673

### EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with George Ballas on 12-01-2005.

The application has been amended as follows:

1) Cancel Claim 15.

### ***Allowable Subject Matter***

2. Claims 1-14 and 16-20 are allowed.

3. The following is an examiner's statement of reasons for allowance: The present invention is directed to a liquid crystal display (LCD) device loading unit comprises: at least one panel jig for holding a LCD device, the LCD having a gate pad, data pad, common pad, and at least one P-MOS device; the panel jig including first, second and third power input terminals; the first power input terminal corresponding to the gate pad, the second power input terminal corresponding to the data pad, and the third power input terminal corresponding to the common pad; the first, second, and third power input terminals contacting the gate, data, and common pads, respectively, when the LCD device is loaded into the panel jig; and the panel jig supplying an OFF-state stress to the P-MOS device of the LCD device via the first, second, and third power input terminals and the gate, data, and common pads as claimed in Claim 16.

The present invention is directed to an apparatus for applying an OFF-state stress to a P-MOS device of a liquid crystal display (LCD) device, comprising: a power supply unit for supplying a source power; a panel loading unit including a plurality of panel jigs on which one of the array substrate and an LCD panel is loaded; a voltage control unit including a plurality of voltage control channels and modulating a voltage of the source power; a frequency control unit including a plurality of frequency control channels and modulating a frequency of the source power; a time setting unit for controlling a time period of supplying the source power; and a panel selecting unit including a plurality of panel selecting channels and modulating an application of the source power as claimed in Claim 1.

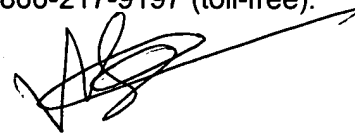
The present invention is directed to a method for applying an OFF-state stress to a P-MOS device of a liquid crystal display (LCD) device, comprising: loading a plurality of array substrates for an LCD device into a plurality of jigs, each of the array substrates having a gate line, a data line, and a common line; applying a source power from a power supply unit to the gate line, data line, and common line of a plurality of the array substrates loaded into the jigs; modulating the source power voltage of the gate line, data line, and common line of each of the plurality of array substrates via a voltage control unit; and modulating the frequency of an AC voltage component of the voltage-modulated source power via a frequency control unit as claimed in Claim 10.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



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